



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mokhlesi
Title: Noise Reduction Technique for Transistors and Small Devices Utilizing an Episodic Agitation
Application No.: 10/052,924 Filing Date: January 18, 2002
Examiner: Hoang, Huan Group Art Unit: 2818
Docket No.: SNDK.256US0 Conf. No.: 1035

November 24, 2003

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicant(s) call(s) the documents listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application.

Copies of the documents listed on the accompanying Form PTO-1449 are enclosed.

Citation of these documents shall not be construed as (1) an admission that the documents are prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(c). A check including \$180.00 for the information disclosure statement fee under 37 C.F.R. § 1.17(p), is enclosed. The Commissioner is authorized, however, to charge any fee that may

SNDK.256US0

Serial No.: 10/052,924

be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

EXPRESS MAIL
LABEL NO.:

EV 321705352 US

Respectfully submitted,



Gerald P. Parsons

Reg. No. 24,486

PARSONS HSUE & DE RUNTZ LLP

655 Montgomery Street, Suite 1800

San Francisco, CA 94111

(415) 318-1160

(415) 693-0194 fax

November 24, 2003

Date

U.S. Department of Commerce, Patent and Trademark	Att'y. Docket No.	Application No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT NOV 24 2003 (Use several sheets if necessary)	SNDK.256US0	10/052,924
	Applicant(s)	Conf. No.
	Mokhlesi	1035
	Filing Date	Group
	January 18, 2002	1035

U.S. Patent Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

U.S. Published Patent Application Documents

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate

Foreign Patent Documents

							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	1	WO 01/27931 A1	4/19/01	PCT				
	2	WO 95/19047	7/13/95	PCT				

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

3	Ma, "A Dual-Bit Split-Gate EPPROM (DSG) Cell in Contactless Array for Single-Vcc High Density Flash Memories," <i>Technical Digest</i> , Electron Device Meeting, 1994, pp. 3.5.1 – 3.5.4.
4	International Search Report mailed October 10, 2003.

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.